

1. (Twice amended) A circuit for removing noise on a voltage input line, comprising:

B1 a ferrite bead connected in the voltage input line, the ferrite bead having a first resistance; and

a bulk capacitor with low equivalent series resistance connected between an output side of the ferrite bead and ground.

2. (Amended) The circuit according to claim 1, wherein the capacitor is a D case tantalum bulk capacitor.

3. (Amended) The circuit according to claim 2, wherein the capacitor has the series resistance of approximately 0.8 Ohms.

4. (Amended) The circuit according to claim 1, wherein the ferrite bead has the first resistance of approximately 0.3 Ohms.

5. (Twice amended) A voltage supply device comprising:
a voltage source including a voltage regulator section producing a voltage output;

a ferrite bead connected at one side to the voltage output and forming at another side an output, the ferrite bead having a first resistance; and

a capacitor with low equivalent series resistance connected between the output and ground;

where switching regulator noise from the voltage regulator section is removable by the ferrite bead and capacitor.

6. (Amended) The voltage supply device according to claim 5, wherein the capacitor is a D case tantalum bulk capacitor.

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7. (Amended) The voltage supply device according to claim 5, wherein the capacitor has the series resistance of approximately 0.8 Ohms.

8. (Amended) The voltage supply device according to claim 5, wherein the ferrite bead has the first resistance of approximately 0.3 Ohms.

9. (Twice amended) A method of removing switching regulator noise from a voltage supply line, comprising:

connecting a ferrite bead in the voltage input line, the ferrite bead having a first resistance; and

connecting a bulk capacitor with low equivalent series resistance between an output side of the ferrite bead and ground.

10. (Amended) The method according to claim 9, wherein the capacitor is a D case tantalum bulk capacitor.

11. (Amended) The method according to claim 10, wherein the capacitor has the series resistance of approximately 0.8 Ohms.

12. (Amended) The method according to claim 9, wherein the ferrite bead has the first resistance of approximately 0.3 Ohms.

13. (Twice amended) A voltage source for a clock circuit, comprising:

a voltage regulator having a regulator output;

a ferrite bead connected to the regulator output of the voltage regulator and having an output, the ferrite bead having a first resistance; and

a bulk capacitor with low equivalent series resistance connected to the output of the ferrite bead at one side and ground at another side;

wherein the ferrite bead and capacitor act to remove switching regulator noise so as to produce an input voltage supply having a reduced switching regulator noise for the clock circuit.

14. (Amended) The voltage source according to claim 13, wherein the capacitor is a D case tantalum bulk capacitor.

15. (Amended) The voltage source according to claim 14, wherein the capacitor has the series resistance of approximately 0.8 Ohms.

16. (Amended) The voltage source according to claim 13, wherein the ferrite bead has the first resistance of approximately 0.3 Ohms.

17. The circuit according to claim 1, wherein the ferrite bead and the capacitor are provided in an "L" shaped filter configuration.

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18. The circuit according to claim 1, wherein the ferrite bead and the capacitor form a resistor divider circuit to assist in noise signal removal.

19. The voltage supply device according to claim 5, wherein the ferrite bead and the capacitor are provided in an "L" shaped filter configuration.

20. The voltage supply device according to claim 5, wherein the ferrite bead and the capacitor form a resistor divider circuit to assist in noise signal removal.

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21. The method according to claim 9, wherein the ferrite bead and the capacitor are provided in an "L" shaped filter configuration.

22. The method according to claim 9, wherein the ferrite bead and the capacitor form a resistor divider circuit to assist in noise signal removal.

23. The voltage source according to claim 14, wherein the ferrite bead and the capacitor are provided in an "L" shaped filter configuration.

24. The voltage source according to claim 14, wherein the ferrite bead and the capacitor form a resistor divider circuit to assist in noise signal removal.
